

## Refine Search

### Search Results -

Term	Documents
SUBSTRATE	467820
SUBSTRATES	188314
CONTROL	1680987
CONTROLS	581596
CIRCUIT	935841
CIRCUITS	491137
MEMORY	454502
MEMORIES	89909
MEMORYS	125
STORAGE	666707
STORAGES	2923
(PIXEL\$1 SAME SUBSTRATE SAME CONTROL ADJ CIRCUIT SAME (MEMORY OR STORAGE)).USPT.	70

There are more results than shown above. Click here to view the entire set.

Database: 
 US Pre-Grant Publication Full-Text Database  
 US Patents Full-Text Database  
 US OCR Full-Text Database  
 EPO Abstracts Database  
 JPO Abstracts Database  
 Derwent World Patents Index  
 IBM Technical Disclosure Bulletins

Search: L9

**Refine Search**

**Recall Text**

**Clear**

**Interrupt**

### Search History

DATE: Wednesday, August 04, 2004   [Printable Copy](#)   [Create Case](#)

Set  
Name   Query  
side by  
side

Hit  
Count   Set  
result set

h   e b   b   c g b   e e c h

*DB=USPT; PLUR=YES; OP=ADJ*

<u>L9</u>	pixel\$1 same substrate same control adj circuit same (memory or storage)	70	<u>L9</u>
<u>L8</u>	pixel\$1 same substrate same control adj circuit	321	<u>L8</u>
<u>L7</u>	pixel\$1 same substrate	14860	<u>L7</u>
<u>L6</u>	L2 and pixel\$1 and substrate	1	<u>L6</u>
<u>L5</u>	L2 and pixel\$1 near4 substrate	0	<u>L5</u>
<u>L4</u>	L2 and pixel\$1 same substrate	0	<u>L4</u>
<u>L3</u>	L2 and substrate\$1	1	<u>L3</u>
<u>L2</u>	5841497.pn.	1	<u>L2</u>
<u>L1</u>	58451497.pn.	0	<u>L1</u>

END OF SEARCH HISTORY

## Refine Search

### Search Results -

Term	Documents
CONTROLLER	427671
CONTROLLERS	91658
(L16 AND CONTROLLER).PGPB,USPT.	39
(L16 AND CONTROLLER).PGPB,USPT.	39

Database:

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Search:

L1 and control\$3

Refine Search

Recall Text

Clear

Interrupt

### Search History

DATE: Wednesday, August 04, 2004   [Printable Copy](#)   [Create Case](#)

<u>Set Name</u> side by side	<u>Query</u>	<u>Hit Count</u>	<u>Set Name</u> result set
<i>DB=PGPB,USPT; PLUR=YES; OP=ADJ</i>			
<u>L19</u>	L16 and controller	39	<u>L19</u>
<u>L18</u>	L16 and memory adj controller	0	<u>L18</u>
<u>L17</u>	L16 and (memory or storage) adj control\$3	0	<u>L17</u>
<u>L16</u>	L15 and substrate\$1	39	<u>L16</u>
<u>L15</u>	L1 and data adj line adj drive same data adj (memoroy or storage)	40	<u>L15</u>
<u>L14</u>	L9 and substate\$1	0	<u>L14</u>
<u>L13</u>	pixel\$1 same substrate same memory adj control\$3 same data	11	<u>L13</u>
<u>L12</u>	pixel\$1 same substrate same memory adj control\$3	29	<u>L12</u>
<u>L11</u>	pixel\$1 same substrate	24572	<u>L11</u>

<u>L10</u>	L1 and memory adj control adj circuitry same data adj line\$1	1	<u>L10</u>
<u>L9</u>	L1 and memory adj control adj circuitry and data adj line\$1	23	<u>L9</u>
<u>L8</u>	L1 and memory adj control adj circuitry and data adj line adj driver\$1	0	<u>L8</u>
<u>L7</u>	L1 and memory adj control adj circuitry same data adj line adj driver\$1	0	<u>L7</u>
<u>L6</u>	L1 and memory adj control adj circuitry and data adj line adj driver\$1	0	<u>L6</u>
<u>L5</u>	L1 and memory adj control adj circuitry	91	<u>L5</u>
<u>L4</u>	L3 and memory adj control adj circuitry	0	<u>L4</u>
<u>L3</u>	L2 and data adj line adj driver\$1 same data adj (memory or storage)	4	<u>L3</u>
<u>L2</u>	L1 and data adj line adj driver\$1	257	<u>L2</u>
<u>L1</u>	pixel\$1	134833	<u>L1</u>

END OF SEARCH HISTORY